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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/769,799	10/769,799 02/03/2004		Jang-Hyoun Youm	1572.1181	9254
21171	7590	05/02/2006		EXAMINER	
STAAS &	HALSEY	LLP	IP, SHIK LUEN PAUL		
SUITE 700 1201 NEW	YORK AV	'ENUE, N.W.		ART UNIT	PAPER NUMBER
WASHING			2837		
				DATE MAILED: 05/02/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Application No. Applicant(s)					
		10/769,799	YOUM, JANG-HYOUN					
	Office Action Summary	Examiner	Art Unit					
		Paul Ip	2837					
Period fo	The MAILING DATE of this communication ap or Reply	opears on the cover sheet with the	correspondence ad	ddress				
WHIC - Exter after - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR REPLEMENTED IN CHEVER IS LONGER, FROM THE MAILING Insions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication, operiod for reply is specified above, the maximum statutory period re to reply within the set or extended period for reply will, by staturely received by the Office later than three months after the mailing patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATIO .136(a). In no event, however, may a reply be tid d will apply and will expire SIX (6) MONTHS from te, cause the application to become ABANDONI	N. mely filed n the mailing date of this o ED (35 U.S.C. § 133).					
Status								
1)🛛	Responsive to communication(s) filed on 16 /	August 2005.						
		is action is non-final.						
3) 🗌	Since this application is in condition for allows		osecution as to the	e merits is				
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Dispositi	on of Claims							
4) 🖂	Claim(s) 1.3-15 and 17 is/are pending in the	application.						
	4a) Of the above claim(s) is/are withdra	• •		,				
5) 🗌	Claim(s) is/are allowed.							
6)⊠	Claim(s) <u>1,3-15 and 17</u> is/are rejected.							
7)	Claim(s) is/are objected to.							
8) 🗌	Claim(s) are subject to restriction and/or election requirement.							
Applicati	on Papers							
9)	The specification is objected to by the Examin	er.						
10)	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority u	ınder 35 U.S.C. § 119							
	Acknowledgment is made of a claim for foreig ☑ All b) ☐ Some * c) ☐ None of:	n priority under 35 U.S.C. § 119(a	ı)-(d) or (f).					
	1.⊠ Certified copies of the priority documents have been received.							
	2. Certified copies of the priority documer	• •						
	3. Copies of the certified copies of the prior		ed in this National	Stage				
	application from the International Burea	, , , ,						
* 8	see the attached detailed Office action for a lis	t of the certified copies not receive	ed.					
Attachmen	` '	_						
1) Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary Paper No(s)/Mail D						
3) 🛛 Inforr	nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08	5) Notice of Informal I		O-152)				
rape	Paper No(s)/Mail Date <u>12/01/2005</u> . 6)							

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DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on 2/3/2004 in compliances with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:
 - 1. Determining the scope and contents of the prior art.
 - 2. Ascertaining the differences between the prior art and the claims at issue.
 - 3. Resolving the level of ordinary skill in the pertinent art.
 - 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.
- 5. Claims 1, 3-15, and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Takagi et al (6,311,801) in view of Shimogama (6,498,448).

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With respect to claims 1, 5, 13, 14, and 17, Takagi et al show in figure 1 a power source 29, an auxiliary power source means 31, and a brake releasing means 30. Takagi et al show in figure 4 a high voltage +H, a low voltage +L, a switching unit 33, and a controller 34 for controlling the switching unit 33. A voltage-boost control circuit 33b provides boosted voltage +L to a higher voltage to a brake coil 14 of a motor. Since the voltage-boost control circuit boosted the +L voltage to charge capacitor 33f with the induced lower voltage from the inductor 33d, the function of Takagi et al is the same as the low voltage connection to the inductor as recited in the claims. Since Takagi et al disclose at column 8 lines 24-32 the DC power source 29 may be common to a power source of a low voltage that is used for an electronic circuit for a computer control or the like. Whereas, the claims recite a braking power providing unit to receive an input voltage lower than a braking voltage and to boost the input voltage to the braking voltage to brake a motor. However, the patent to Shimoganma discloses a motor break releasing device comprising a brake B (2) including a lower power supply 20 as shown in figures 6-8. Prima facie case is made that Takagi et al show the voltage boosting and charging circuit 31a or 33 in figures 2 and 4 respectively with a +L voltage power supply. Since the use of a single power supply with H and L power supply, or use a separate H power supply with a L power supply produces the same result, it would have been obvious to one of ordinary skill in the art to provide Takagi et al with a low power supply as taught or suggested by Shimoganma.

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With respect to claims 3, 4, and 6, Takagi et al show in figure 4 an inductor 33d, a diode 33e, and a condenser 33f for providing the functions as recited in the claims respectively.

With respect to claims 5 and 7-11, Takagi et al show in figure 4 that the transistor 34b and the resistors 34a and 34c are AND gate equivalent circuit as recited in the claims.

With respect to claims 12 and 15, Takagi et al show in figure 6 a prior art motor control system comprising a motor control circuit for a motor 2. It is inherent that the motor control circuit includes an inverter for controlling the motor 2.

Citation of Pertinent References

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

The patent or publication to Youm (6,879,123 or 2005/0007048), Eckardt (7,026,783), Baumgartner et al (6,607,251), and Nakamura (2005/0098397) discloses braking control system with a separate power supply for providing power to the brake of the motor.

Communication Information

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul Ip whose telephone number is (571)-272-1941. The examiner can normally be reached on Monday to Friday from 6:30 am to 3:00 pm Eastern time.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Martin, can be reached on (571)-272-2107. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Paul lp

Primary Examiner

Parly

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4/25/2006